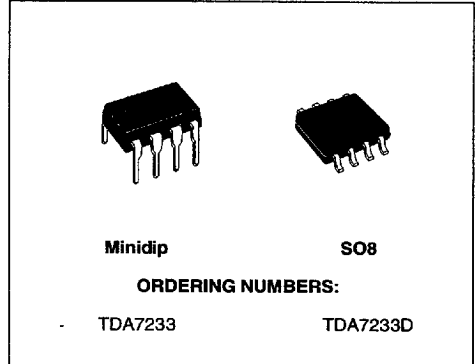


**1W AUDIO AMPLIFIER WITH MUTE**

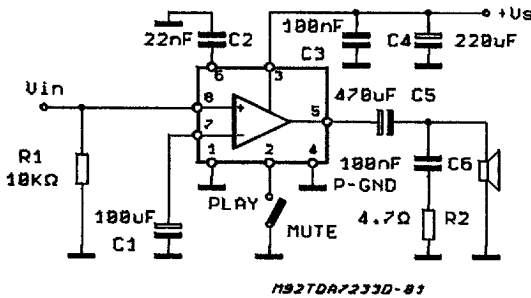
- OPERATING VOLTAGE 1.8 TO 15V
- EXTERNAL MUTE OR POWER DOWN FUNCTION
- IMPROVED SUPPLY VOLTAGE REJECTION
- LOW QUIESCENT CURRENT
- HIGH POWER CAPABILITY
- LOW CROSSOVER DISTORTION

**DESCRIPTION**

The TDA7233/D is a monolithic integrated circuit in 8 pin Minidip or SO8 package, intended for use as class AB power amplifier with a wide range of supply voltage from 1.8V to 15V in portable players, cordless telephones and Cellular Radios.



**TEST AND APPLICATION CIRCUIT**

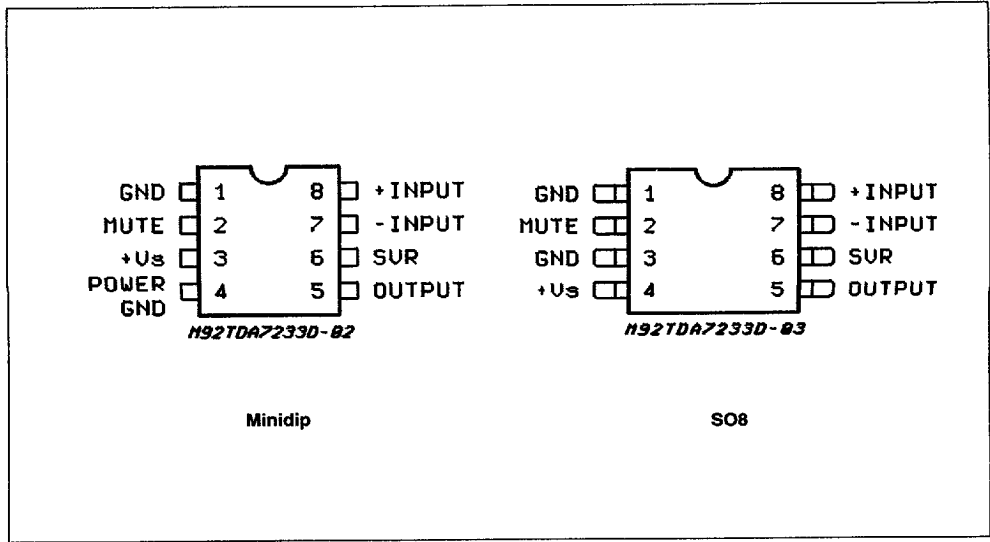


Note: Switch Open = Mute  
Switch Closed = Play

**ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
$V_s$	Supply Voltage	16	V
$I_o$	Output Peak Current	1	A
$P_{tot}$	Total Power Dissipation at $T_{amb} = 50^{\circ}C$	1	W
$T_{stg}, T_j$	Storage and Junction Temperature	-40 to 150	$^{\circ}C$

**PIN CONNECTIONS (Top views)**



**THERMAL DATA**

Symbol	Parameter	SO8	Minidip	Unit
$R_{th j-amb}$	Thermal Resistance Junction-ambient Max.	200	100	$^{\circ}C/W$

7929237 0064476 090

2/4

240



ELECTRICAL CHARACTERISTICS ( $V_s = 6\text{ V}$ ,  $T_{\text{amb}} = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$V_s$	Supply Voltage		1.8		15	V
$V_o$	Quiescent Out Voltage			2.7		V
		$V_s = 3\text{ V}$		1.2		V
		$V_s = 9\text{ V}$		4.2		V
$I_d$	Quiescent Drain Current	MUTE HIGH		3.6	9	mA
		MUTE LOW		0.4		
$I_b$	Input Bias Current			100		nA
$P_o$	Output Power	$d = 10\%$ $f = 1\text{ kHz}$ $V_s = 12\text{ V}$ $R_L = 8\ \Omega$ $V_s = 9\text{ V}$ $R_L = 4\ \Omega$ $V_s = 9\text{ V}$ $R_L = 8\ \Omega$ $V_s = 6\text{ V}$ $R_L = 8\ \Omega$ $V_s = 6\text{ V}$ $R_L = 4\ \Omega$ $V_s = 3\text{ V}$ $R_L = 4\ \Omega$ $V_s = 3\text{ V}$ $R_L = 8\ \Omega$		1.9 1.6 1 0.4 0.7 110 70		W W W W W mW mW
$d$	Distortion	$P_o = 0.5\text{ W}$ $f = 1\text{ kHz}$ $R_L = 8\ \Omega$ $V_s = 9\text{ V}$		0.3		%
$G_v$	Closed Loop Voltage Gain	$f = 1\text{ kHz}$		39		dB
$R_{IN}$	Input Resistance	$f = 1\text{ kHz}$	100			$K\Omega$
$e_N$	Total Input Noise ( $R_s = 10\text{ k}\Omega$ )	$B = \text{Curve A}$		2		$\mu\text{V}$
		$B = 22\text{ Hz to } 22\text{ kHz}$		3		
SVR	Supply Voltage Rejection	$f = 100\text{ Hz}$ , $R_G = 10\text{ K}\Omega$		45		dB
	MUTE Attenuation	$V_o = 1\text{ V}$ $f = 100\text{ Hz to } 10\text{ kHz}$		70		dB
	MUTE Threshold			0.6		V
$I_M$	MUTE Current	$V_s = 15\text{ V}$		0.4		mA

■ 7929237 0064477 T27 ■

Figure 1: Output Power vs. Supply Voltage

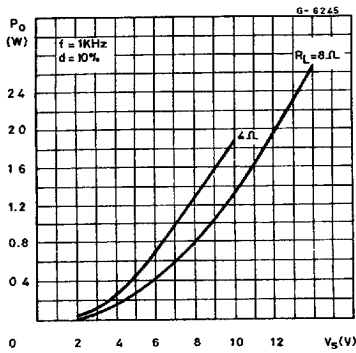


Figure 3: DC Output Voltage vs. Supply Voltage

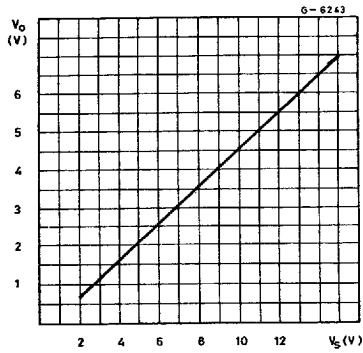


Figure 5: Total Dissipated Power vs. Supply Voltage

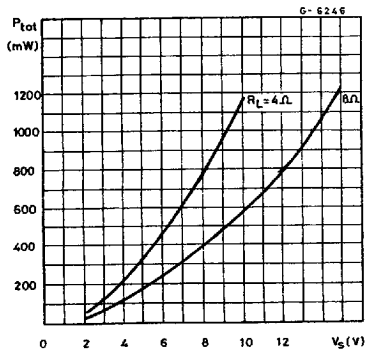


Figure 2: Supply Voltage Rejection vs. Frequency

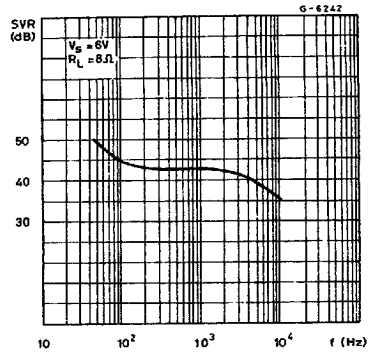
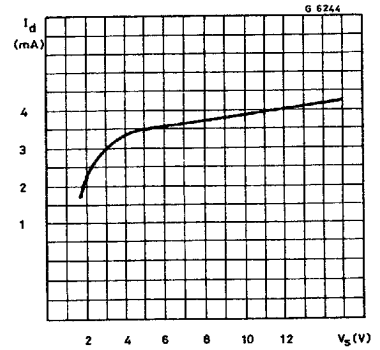


Figure 4: Quiescent Current vs. Supply Voltage



79292237 0064478 963